

- A) A FOUR BIT D TO A CONVERTER IS USED IN CONJUNCTION WITH ADAD TO GIVE 12 BIT RESOLUTION TO THE COUNTER.
- B) GATING LOGIC TO PAISE OR LOWER THE SET POINT AND THE PULSE GENERATOR WHICH FEEDS THE COUNTER AT A RATE DETERMINED BY LOGIC INPUT.
- C) AN OUTPUT AMPLIFIER NETWORK RESPONDING TO THE COUNTER ANALOG SIGNAL TO GIVE THE DIGITAL SET POINT FOR SPEED CONTROL.
- D) A. SIGNAL GENEPAIOR IS INCLUDED TO GIVE UNDERSPEED AND OVERSPEED TEST SIGNALS.